

Amendments of the Specification

Please replace the title with the following amended title:

DSP CIRCUITRY FOR SUPPORTING MULTI-CHANNEL
APPLICATIONS BY SELECTIVELY SHIFTING DATA THROUGH REGISTERS

Please replace paragraph [0030] with the following amended paragraph:

[0030] DSP utilization circuitry 140 includes circuitry (e.g., multiplier circuits, adder circuits, etc.) to operate on the data provided by circuitry 100. DSP utilization circuitry 140 is typically responsible for enabling DSP circuitry 20 to perform a desired DSP operation. For example, circuitry 140 may be constructed or be configurable to perform arithmetic operations of integers, real numbers, and imaginary numbers of various length (e.g., 9 bit, 18 bit, or 27 bit arithmetic). Circuitry 140 may enable DSP circuitry 20 to perform the operations needed to provide a FIR filter (e.g., a symmetrical, even order, or odd-order FIR filter). The complexity of DSP utilization circuitry 140 can range from simple circuitry such as multipliers to more complex circuitry such as hybrid multipliers. Examples of hybrid multipliers can be found, for example, in Esposito et al., U.S. Patent Application No. 10/712,500, filed, November 12, 2003, ~~(Attorney Docket No. 174/257)~~, the disclosure of which is incorporated in its entirety. Examples of other types of DSP utilization circuitry can found in Langhammer et al., U.S. Patent Application No. 10/678,201, filed October 3, 2003, ~~(Attorney Docket~~

~~No. 174/251)~~, the disclosure of which is incorporated in its entirety. (FIG. 4 shows a schematic diagram of an embodiment of DSP utilization circuitry 140 that can be used to provide DSP circuitry 20 with FIR filtering operations.)